VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS), HYDERABAD

Accredited by NAAC with A++ Grade

B.E. IV-Semester Main & Backlog Examinations, July-2023 P Spice Modeling for Electrical Circuits

Time: 3 hours

Max. Marks: 60

Note: Answer all questions from Part-A and any FIVE from Part-B

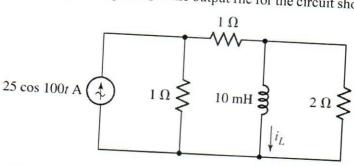
	Part-A ($10 \times 2 = 20 \text{ Marks}$)	M	L	CO	PO
). No.	Stem of the question	2	1	1	1,2
1.	Give an example of Textual Scientific Notation which is used in PSPICE software.		1	1	1,2
	W. to property of voltage dependent voltage source in PSPICE A/D software	2	1	2	
 3. 	Write the part name for a voltage-controlled current source in PSFICE ALD	2	1	2	1,2
	software	2	1	2	1,2
4.	Write any two applications of PSPICE A/D software	2	1	3	1,2
5.	Explain the significance of .TRAN statement to analyze AC electrical circuits	2	1	3	1,2
6.	Write the syntax of sinusoidal AC voltage source	2	1	4	1,2
7.	D. J. DI OT command		1	4	1,2
	What is the use of PRINT statement in a circuit file in PSPICE A/D software	2	1		1,2
8.	Write the syntax for DC parametric sweep statement in electrical circuits	2	1	5	
9.	Describe the use of .MODEL statement in PSPICE A/D software	2	1	5	1,2
10.	Describe the use of MODEL statement in A Describe the use of A Describes the use of				
		4	2	1	1,2
11. 8	Explain the format of output file in PSPICE A/D software.	4	3	1	1,2
ł	Write a PSpice program to print i in the circuit shown below.				
**	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$				
12.	a) Describe the format of a circuit file in PSPICE A/D software with a suitab	le 4	1 3	2 2	1,2
12.	evample		1	3 2	2 1,2
	b) Write a PSpice program to print v ₀ and i ₀ in the circuit shown below.				
	15 A \bullet 2 Ω $\stackrel{i_0}{\geqslant}$ $\stackrel{i_0}{\downarrow}$ 12 Ω $\stackrel{+}{\geqslant}$ $\stackrel{v_0}{\downarrow}$				

2

3

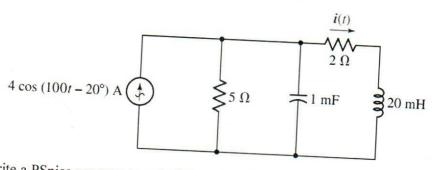
1,2

13. a) Write a PSpice program to print i_L in the output file for the circuit shown below



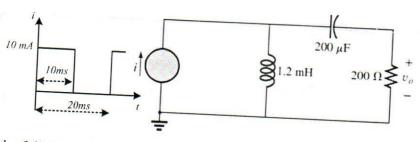
Write a PSpice program to print i(t) and power dissipated in 5Ω in the output file for the circuit shown below





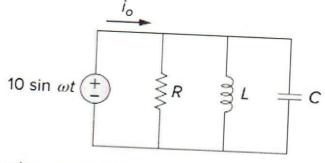
14. a) Write a PSpice program to print harmonic voltages in v0 up to 15th harmonic for the circuit shown below.





In the following circuit, the frequency of source is varied from 50 Hz to 100 kHz with a decade increment and 20 points per decade. Write a PSpice program to print current through R, if C varies from 5 μ F to 10 μ F with a step increment of 1 μ F. Let R = 8 k Ω and L = 0.2 mH.





For NPN transistor, write a PSpice program to plot output characteristics if VCE is varied from 0 to 12 V insteps of 0.02 V and IB is varied from 0 to 1 mA insteps of 200 μ A. Use Q2N2222A NPN transistor with model parameters IS = 3.295E-14, BF = 173, VA = 83.3 V, CJE = 29.6 PF, CJC = 19.4 PF, TF = 489.88 PS, and TR = 4.9 NS.